Attorney Docket No: TSMC2003-0919

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

é Application of: Yi-Hsun Wu et al.

Confirmation No.: 2646

Serial No.: 10/812,378

Group Art Unit: 2836

Filed: March 29, 2004

Examiner: Not Yet Assigned

For: OUTPUT BUFFER ESD PROTECTION USING PARASITIC SCR PROTECTION

CIRCUIT FOR CMOS VLSI INTEGRATED CIRCUITS

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail and addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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